

Freescall Semiconductor Hong Kong Ltd.

2 Dai King St, Tai Po Ind Est, Tai Po, NT, Hong Kong.

Schematic: i.MXS SideShow ADS
Version: 1.3

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Changes from vl.1:

- Zener diode added for surge protection.
- Removed the capacitors across the 10 I/O keys, which were found to cause problem in debounce.

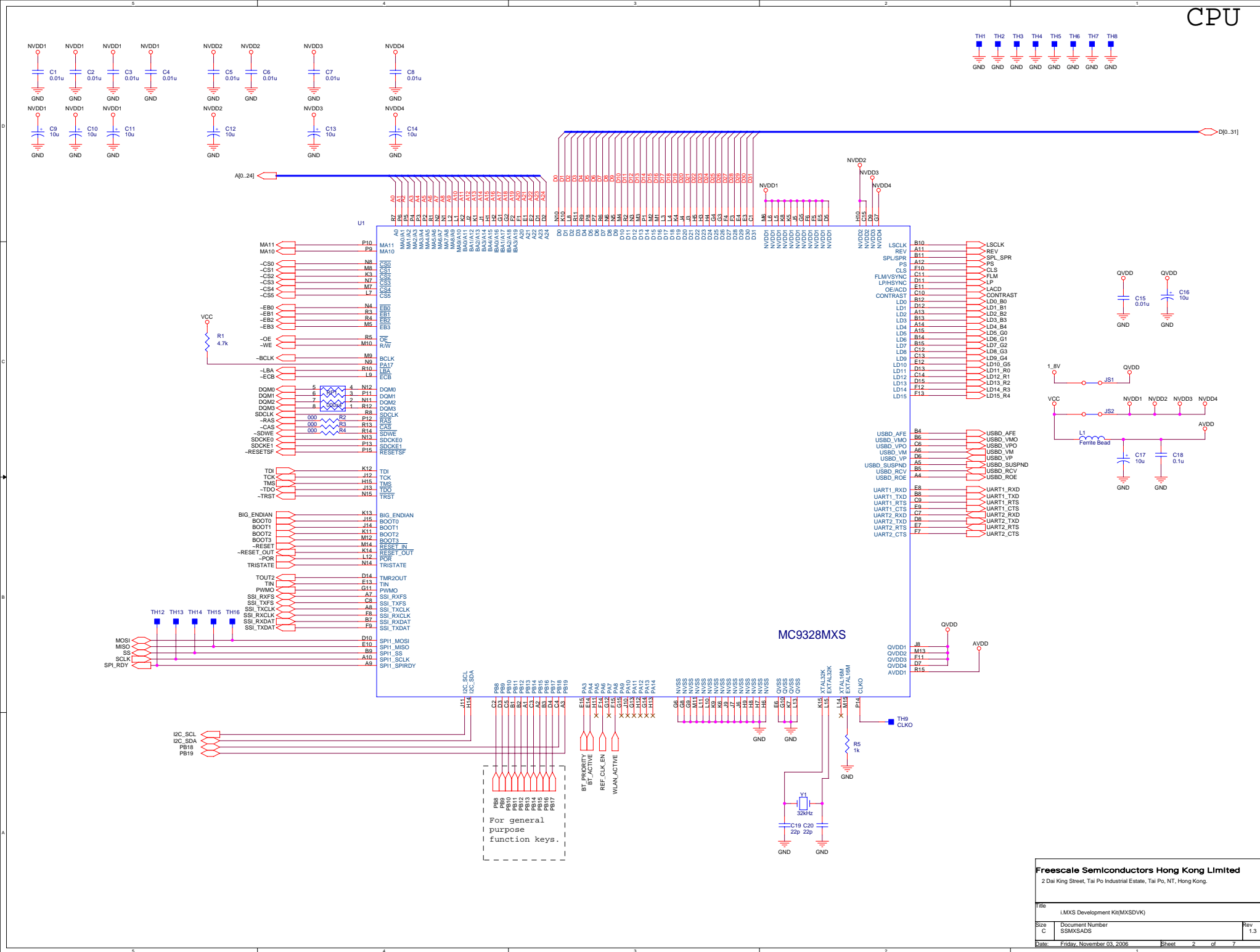
Changes from vl.2:

- MLV Varistor added for ESD protection.
- Removed the MAX1818 and MAX8860
- Added SB1132B18-M5T1G and S-1170B30UC-OTPTFG
- Changed RS232 Transceiver from MAX3245CAI to SP3243ECA
- Cut the trace for ISP1105W internal 3.3V regulator.Connect pin 13 to pin 7 as shown in the sketch.
- Remove SW1,Use two 1N4001 Diode to replace it
- Used a 5.1V ZENER to replace D2 SS32
- Added SPI signal to P5 and P6 connector
- Added R55 AND R56 FOR USB POWER REGULATOR OPTIONAL
- Changed R16,R17 from 1Kohm to 4.7Kohm,R18,R19 from 1Kohm to 10Kohm
- Added diode D5,D6 for reset circuit.
- Added ESD diode on all buttons
- Changed connector P5 from 20 pin to 26 pin for future BT application
- Added 0ohm resistor to all signal line in header P5

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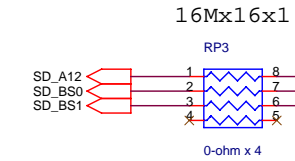
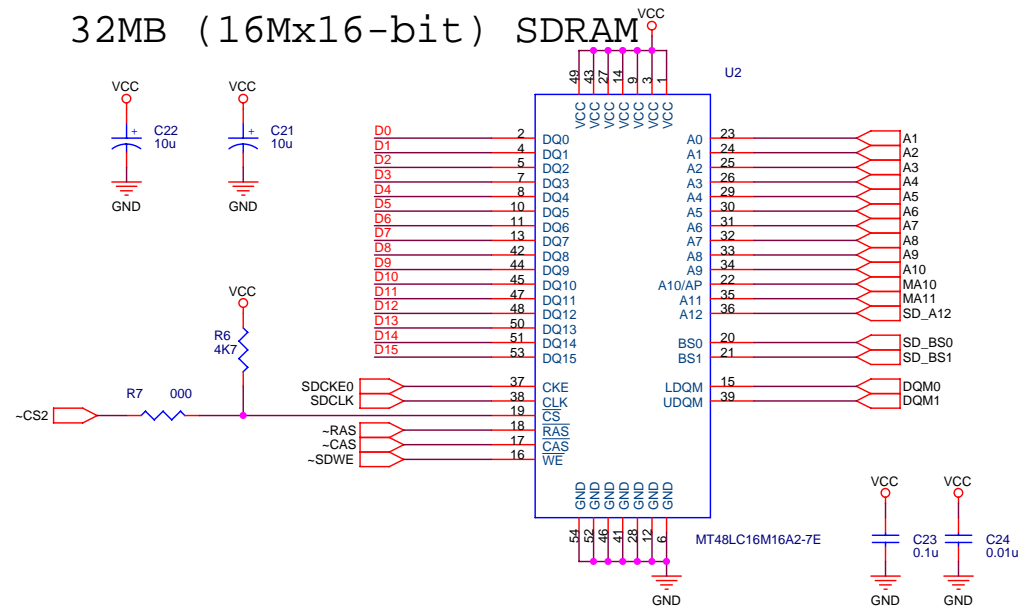
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i.MXS Development Kit(MXSDVK)		
Size A	Document Number SSMXSADS	Rev 1.3
Date:	Friday, November 03, 2006	Sheet 1 of 7

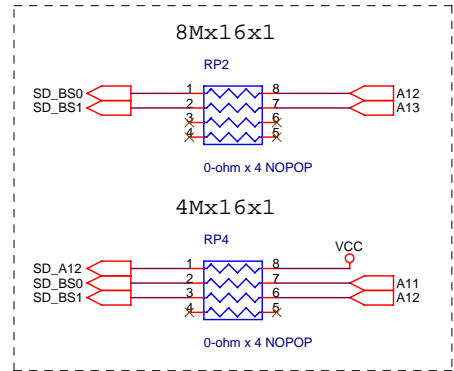


32MB (16Mx16-bit) SDRAM

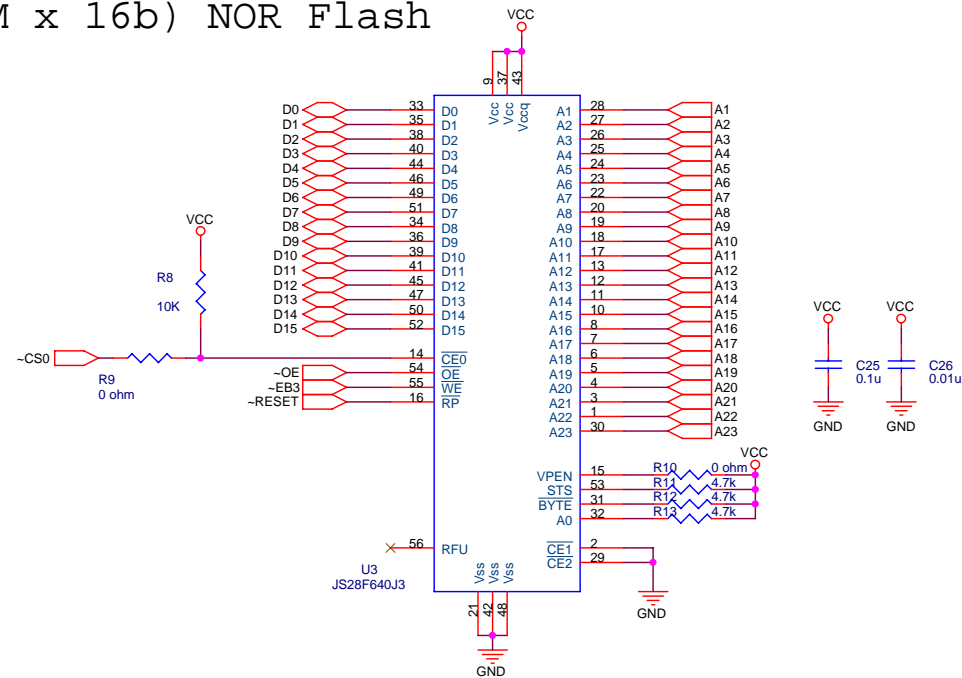
Memory



For optional 8MB or 32MB SDRAM config



8MB (4M x 16b) NOR Flash



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Title
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SSMXSADS

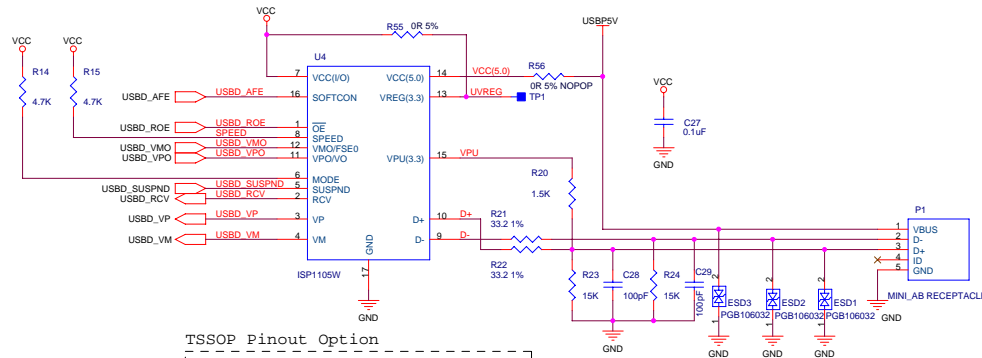
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Date: Friday, November 03, 2006

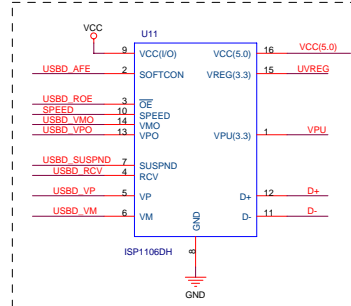
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USB, LCD and I/O keys

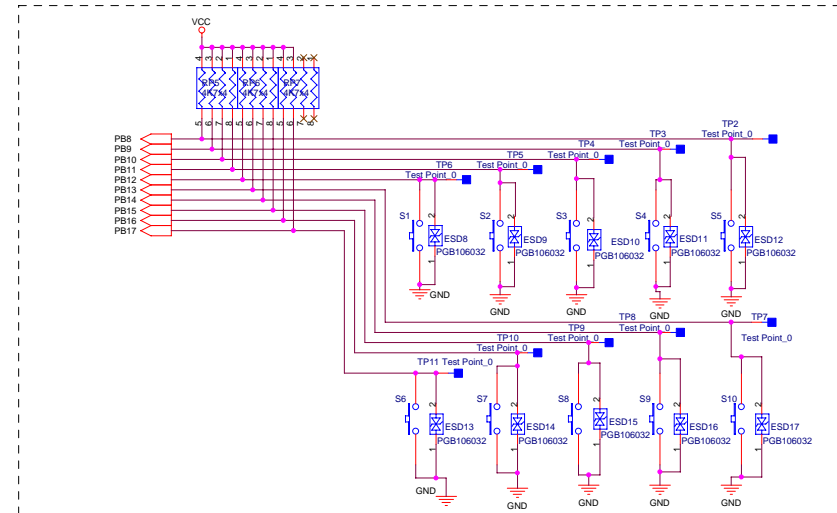
USB DEVICE



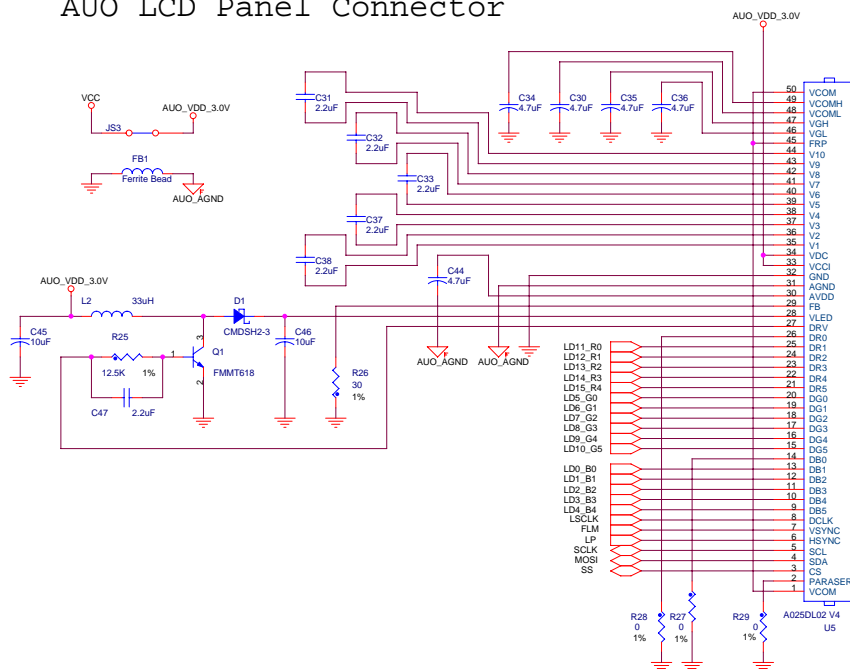
TSSOP Pinout Option



10 GENERAL PURPOSE KEYS



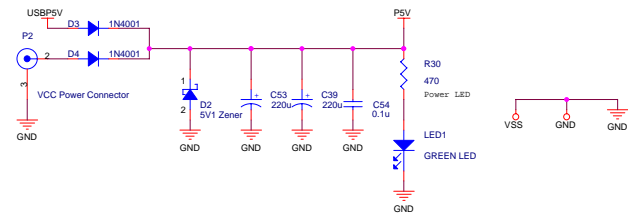
AUO LCD Panel Connector



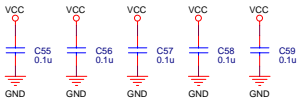
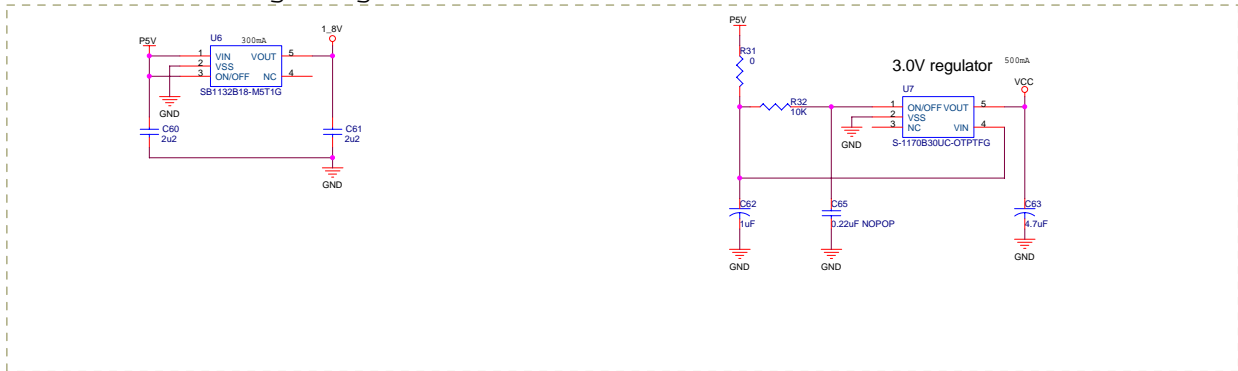
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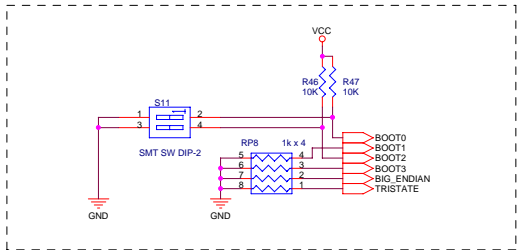
Power Connector



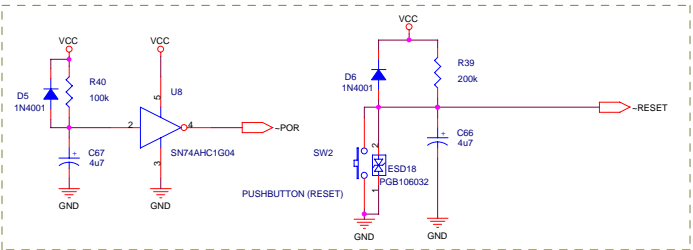
External Voltage Regulator



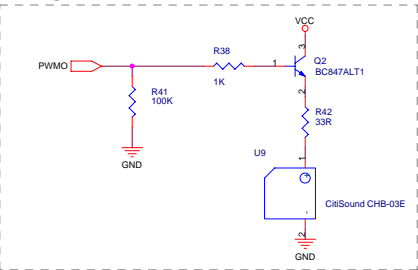
Boot Mode Selection



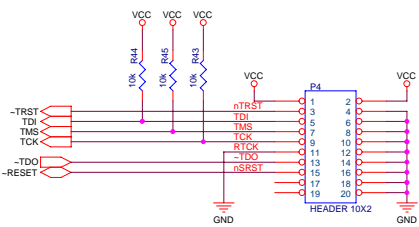
RESET



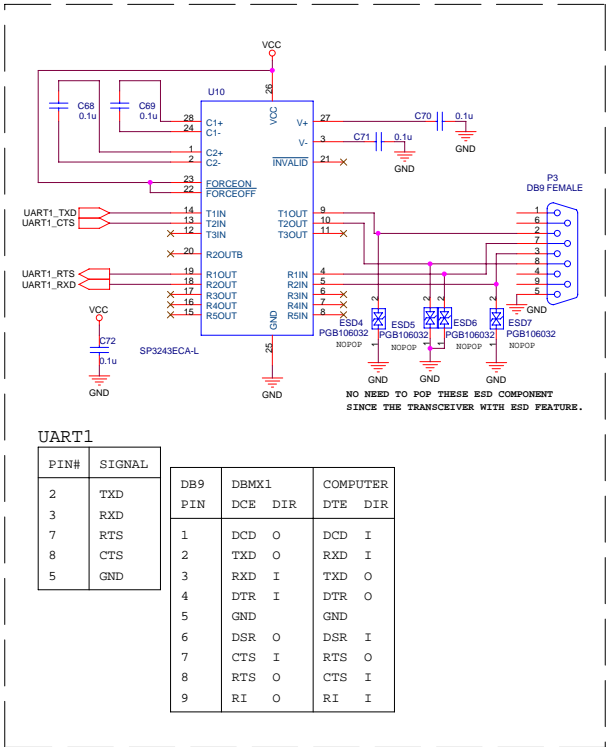
Single Tone Generator



Multi-ICE Interface Connector



UART1 Connection



Extension Connectors

